

FIG. 2

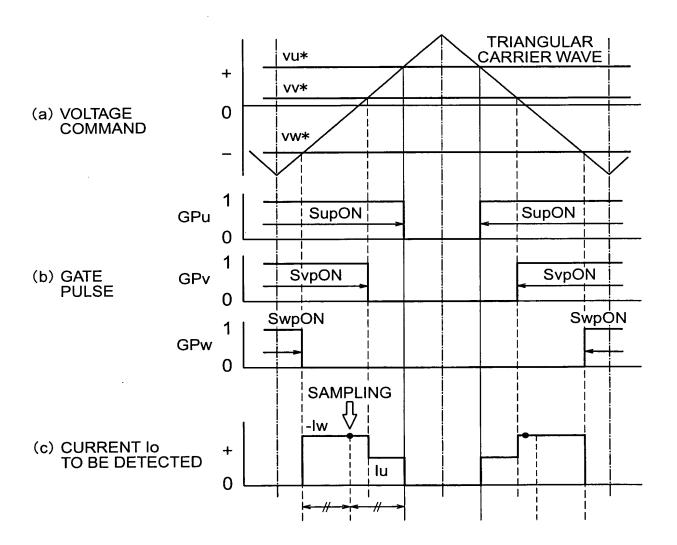


FIG. 3

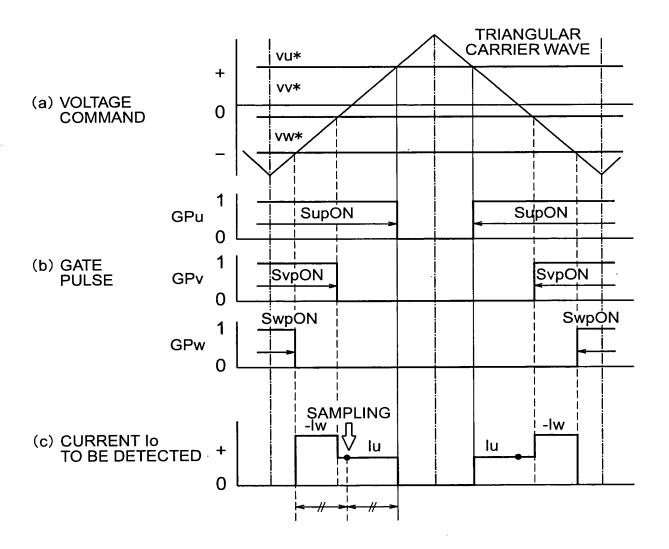


FIG. 4

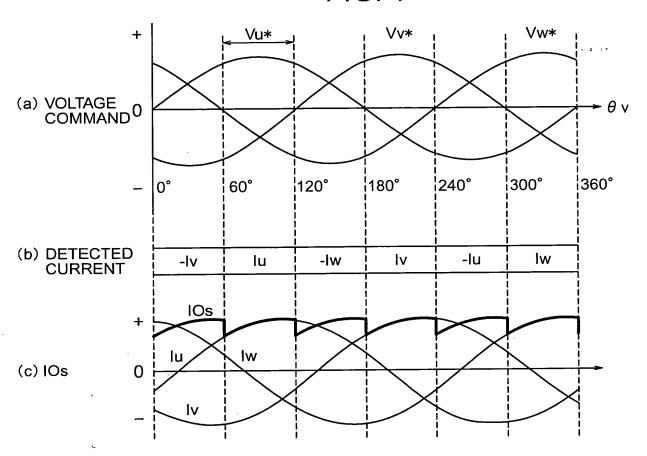


FIG. 5

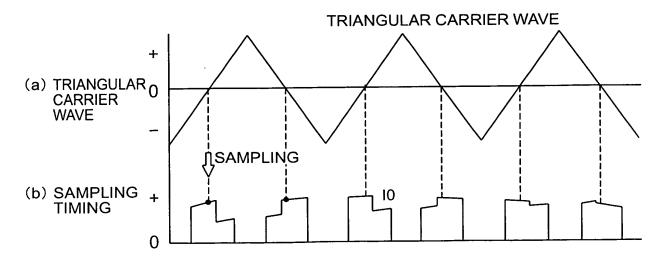


FIG. 6

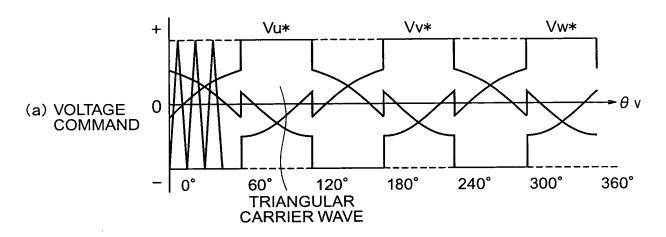
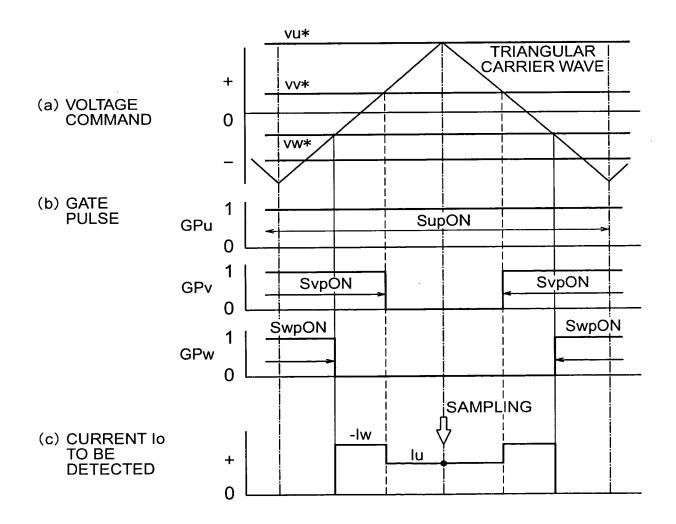
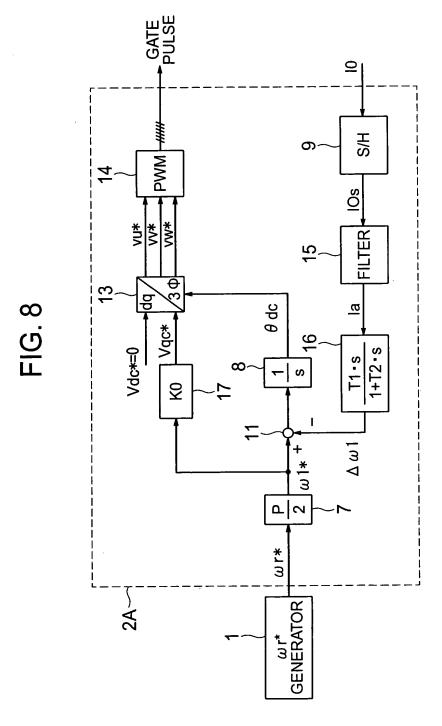
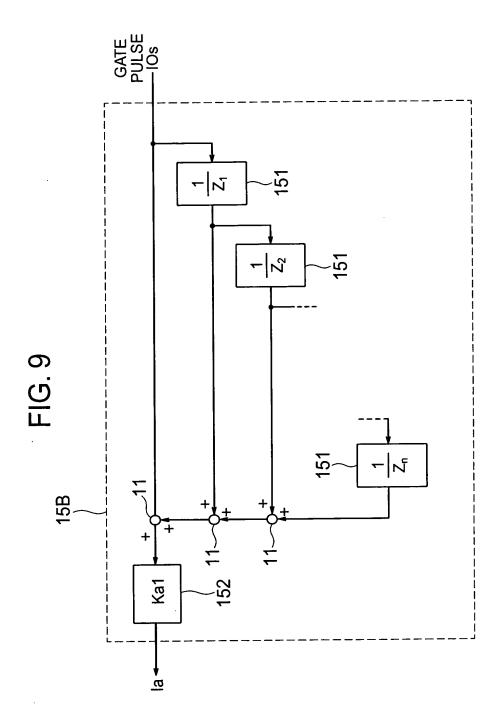


FIG. 7







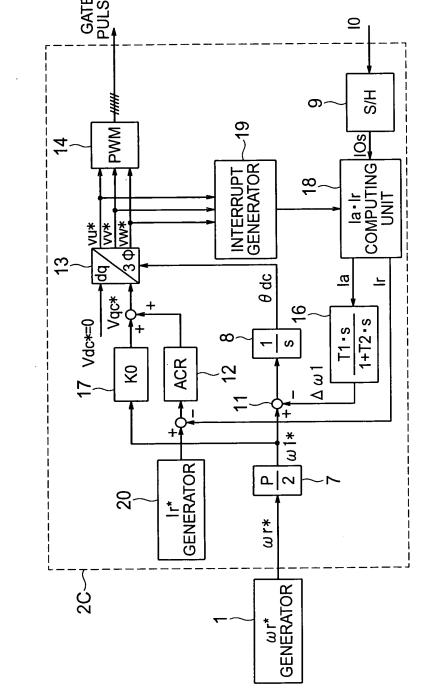
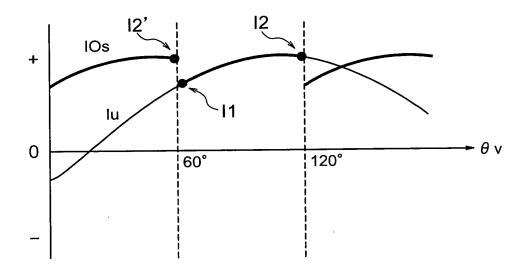
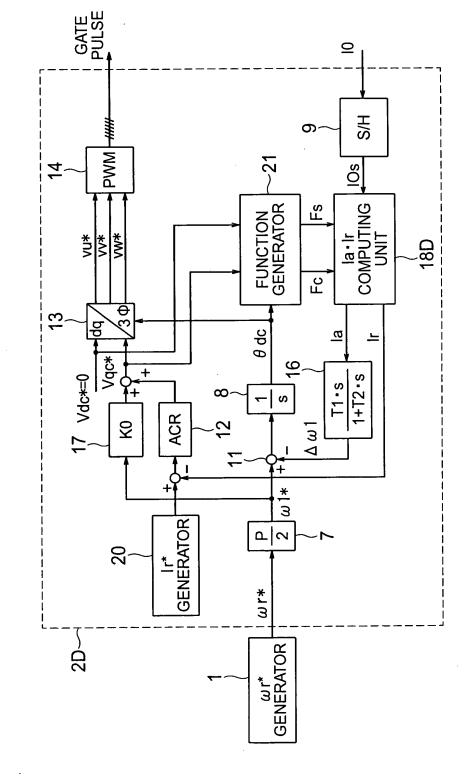


FIG. 10

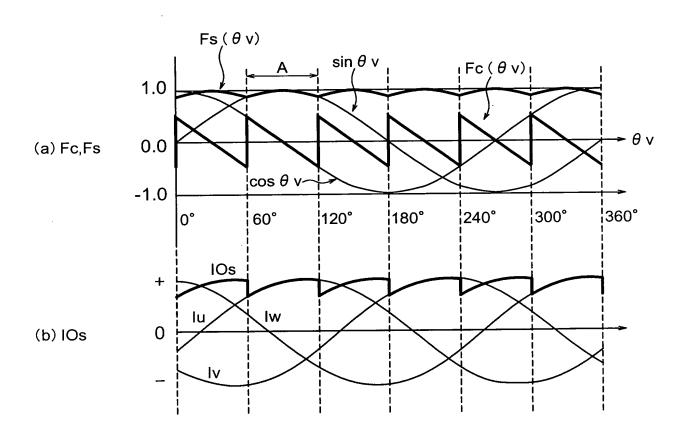
FIG. 11





=1G. 12

FIG. 13



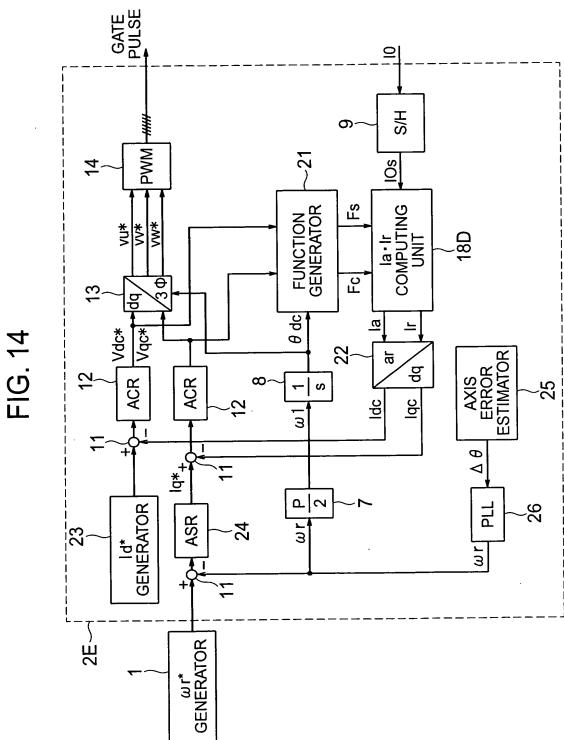
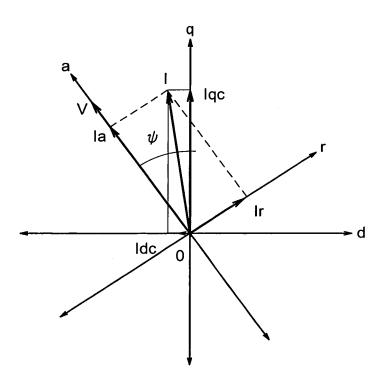


FIG. 15



GATE PULSE $\underline{\circ}$ S/H S/H S/H တ PWM lOs 4 PHASE CURRENT REPRODUCER 27 COMPUTING UNIT FUNCTION GENERATOR *** 18D Fc क ए 33 θ dc FIG. 16 Xqc* Xdc* 22 ä bp 20 ACR ACR S 8 AXIS ERROR ESTIMATOR <u>'</u> 25 29-<u>망</u> Δθς ما GENERATOR ASR ωr <u> 2</u>6 귭 3 2F ~ ωr* GENERATOR

• •

FIG. 17

